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2811



Docket No.: 500055.02 (660073.488D1)
 Date: November 2, 2000

In re application of **Pierre C. Fazan and Gurtej S. Sandhu**
 Application No.: **09/386,646**
 Filed: **August 31, 1999**
 For: **METHOD AND APPARATUS FOR TRENCH ISOLATION PROCESS WITH
 PAD GATE AND TRENCH EDGE SPACER ELIMINATION**

BOX NON-FEE AMENDMENT
 COMMISSIONER OF PATENTS
 WASHINGTON DC 20231

Sir:

Transmitted herewith is an amendment in the above-identified application.

[X] No additional claim fee is required.

	(Col. 1)		(Col. 2)	(Col. 3)	SMALL ENTITY		OR	OTHER THAN A SMALL ENTITY	
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST PREV. PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE		RATE	ADDITIONAL FEE
TOTAL	* 15	MINUS	** 20	-0-	x 9	\$ -0-		x 18	\$ -0-
INDEP.	* 6	MINUS	*** 7	6	x 40	\$ -0-		x 80	\$ -0-
TOTAL ADDITIONAL FEE						\$ -0-	TOTAL		\$ -0-

* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space.

The "Highest Number Previously Paid For" (Total or Independent) is the highest number found from the equivalent box in Col. 1 of a prior amendment or the number of claims originally filed.

- [X] The Commissioner is hereby authorized to charge payment of the following additional fees associated with this communication or credit any overpayment to Deposit Account No. 50-1266. A duplicate copy of this sheet is enclosed.
- [X] Any filing fees under 37 CFR 1.16 for the presentation of extra claims.
- [X] Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

DORSEY & WHITNEY LLP

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I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Box Non-Fee Amendment, Commissioner for Patents, Washington, DC 20231.

Date

November 2, 2000

Lisa Napoli



THE UNITED STATES PATENT AND TRADEMARK OFFICE

Pierre C. Fazan and
Gurtej S. Sandhu

Attorney Docket No.: 500055.02 (660073.488D1)

Serial No. : 09/386,646

Group Art Unit : 2811

Filed : August 31, 1999

Examiner : Hung K. Vu

Title : METHOD AND APPARATUS FOR TRENCH ISOLATION PROCESS WITH PAD GATE AND TRENCH EDGE SPACER ELIMINATION

Box Non-Fee Amendment
Commissioner for Patents
Washington, DC 20231

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AMENDMENT

Sir:

In response to the Office Action dated August 2, 2000, please reconsider the application in view of the following amendments and remarks:

In the Claims:

Please cancel claim 23 and amend the claims 22, 24-26, 28, 30, 32, and 34 as

follows:

Sub D ✓
22. (Twice Amended) A microelectronic device, comprising:
a microelectronic substrate;
a gate structure including a gate oxide layer formed on the substrate, [and] a first gate layer formed on the gate oxide layer, and an adhesion layer composed of a material other than a conductively doped polysilicon material formed on the first gate layer, the gate structure having a trench at least partially disposed therein and extending into the substrate; and

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a field oxide layer at least partially in the trench, the field oxide layer having a field oxide level between the level of an upper surface of the substrate and the level of an upper surface of the first gate layer.

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